

Electronic Acknowledgement Receipt

EFS ID:	2234640
Application Number:	10802199
International Application Number:	
Confirmation Number:	3761
Title of Invention:	Can microcontroller that permits concurrent access to different segments of a common memory by both the processor core and the DMA engine thereof
First Named Inventor/Applicant Name:	Ka Leung Ling
Customer Number:	24737
Filer:	Kenneth DeWayne Springer.
Filer Authorized By:	
Attorney Docket Number:	US000192A
Receipt Date:	24-SEP-2007
Filing Date:	16-MAR-2004
Time Stamp:	18:04:55
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$500
RAM confirmation Number	13892
Deposit Account	500238

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:

Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	US000192A_Transmittal_of_Appeal_Brief.pdf	31507 de5974b95e4d256d195cbac414b3fd1 test9027	no	1
Warnings:					
Information:					
2	Appeal Brief Filed	US000192A_Appeal_Brief_7-723.pdf	156093 e328957fed4013007a915f741402b1d95 foode	no	25
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8225 a1588602950357edde7c15b1c0d754e b910a50	no	2
Warnings:					
Information:					
Total Files Size (in bytes):					195825
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